

PROVISIONAL STATEMENT OF GENERATOR SCHEDULE (EX_BUS) BACKED DOWN FOR THE DATE: 5-Jun-17						
SR. NO.	GENERATING STN. / STOA	Backing Down Period (in Time Block)		TARGET DESPATCHED SCHEDULE (in MW)		REMARK
		FROM	TO	Declared Capacity (A)	Despatched Schedule (Min) (B)	Backing Down Quantum (Max) in MW (A-B)
1	TPC U-6	X	X	X	X	X
2	JSW PTC TO RINFRA DA	X	X	X	X	X
3	NASHIK	1	40	364	283	81
		64	96	364	283	81
4	REL DAHANU U1 AND U2	1	35	464	336	128
		64	96	464	336	128
5	TPC U-8	1	35	235	187	48
		64	96	235	187	48
6	TPC U-5	1	35	473	292	181
		64	70	473	292	181
7	RATTANINDIA U1,2,3,4,5	1	42	X	X	X
		64	96	X	X	X
8	BHUSWAL U2,3	1	42	165	144	21
		64	77	165	144	21
		78	96	X	X	X
9	KORADI U-5 TO U-7	1	42	X	X	X
		64	96	X	X	X
10	VIPL U1 AND U2	1	42	286	191	95
		64	96	286	191	95
11	PARLEY U-8	1	42	X	X	X
		64	96	X	X	X
12	BHUSWAL U-4 AND U-5	2	42	940	644	296
		64	96	940	644	296
13	PARLEY U-6, 7	3	42	458	332	126
		64	96	458	332	126
14	KHAPERKHEDE U1 TO U4	6	42	X	X	X
		64	96	X	X	X
15	KHAPERKHEDE U-5	6	42	432	335	97
		64	96	432	335	97
16	KORADI U8 ,U9,10	7	42	X	X	X
		64	96	X	X	X
17	PARAS U-3 AND U-4	7	42	364	332	32
		64	96	364	332	32
18	ADANI 440 PPA	7	42	X	X	X
		66	79	X	X	X
		83	96	X	X	X
19	ADANI U-1(1200MW & 125MW PPA)	7	42	X	X	X
		66	79	X	X	X
		83	96	X	X	X
20	ADANI U-4(1200MW & 125MW PPA)	7	42	X	X	X
		66	79	X	X	X
		83	96	X	X	X
21	ADANI U-5(1200MW & 125MW PPA)	7	42	X	X	X
		66	79	X	X	X
		83	96	X	X	X
22	CHANDRAPUR U3 TO U7	7	42	1581	1282	299
		66	79	1581	1282	299
		83	96	1581	1282	299
23	CHANDRAPUR U8 AND U9	7	43	767	658	109
		66	79	767	658	109
		83	96	767	658	109
24	ADANI UNIT-2	66	79	X	X	X
		83	96	X	X	X
25	ADANI U-3	66	79	X	X	X
		83	96	X	X	X
26	JSW U-1	67	79	245	200	45

NOTE:

- 1 Above Statement is an abstract of Load Generation Balance as per Day Ahead Schedules, based on State Merit Order Despatch. Maximum backeddown quantum during "Backing down Period" is indicated in the statement. Blockwise variations are available under "View Schedules".
- 2 M.O.D RATES EFFECTIVE FROM 12TH AF May-17
- 3 # Indicates that back down withdrawn due to Line loadings/system constraints.