

PROVISIONAL STATEMENT OF GENERATOR SCHEDULE (EX_BUS) BACKED DOWN FOR THE DATE:							14-Feb-16
SR. NO.	GENERATING STN. / STOA	Backing Down Period (in Time Block)		TARGET DESPATCHED SCHEDULE (in MW)		Backing Down Quantum [Max] in MW [A-B]	REMARK
		FROM	TO	Declared Capacity (A)	Despatched Schedule (Min) (B)		
1	TPC U-6	x	x	x	x	x	UNIT SHUTDOWN
2	NASIK	1	96	536	425	111	AS PER SYSTEM CONDITION
3	KORADI U 5 TO 7	x	x	x	x	x	U5 DECL TECH MIN & U7 SCH ZERO MW BY MSEDCL
4	ADANI TO BEST	X	X	X	X	X	NO SCHEDULE
5	BHUSAWAL U2,3	1	96	353	288	65	AS PER SYSTEM CONDITION
6	BHUSAWAL U-4 & 5	1	96	470	322	148	AS PER MOD U5 SCH ZERO MW BY MSEDCL
7	INDIA BULL U:1,2 ,3, 4,5	1	96	1200	860	340	AS PER SYSTEM CONDITION
8	TPC U-5	1	34	473	292	181	AS PER SYSTEM CONDITION
		41	96	473	292	181	
9	KHAPER KHEDA U 1 TO 4	1	34	724	572	152	AS PER MOD
		41	96	724	572	152	
10	TPC U-8	X	X	X	X	X	UNIT OUT
11	VIPL U1&2	1	34	273	191	82	AS PER SYSTEM CONDITION
		41	96	273	191	82	
12	REL DAHANU U 1&2	1	34	470	336	134	AS PER SYSTEM CONDITION
		41	96	470	336	134	
13	KORADI U8	1	34	620	432	188	AS PER SYSTEM CONDITION
		41	96	620	432	188	
14	CHANDRAPUR	1	26	1703	1282	421	AS PER SYSTEM CONDITION
		82	96	1703	1282	421	
15	KHAPER KHEDA U 5	1	23	475	335	140	AS PER SYSTEM CONDITION
		86	96	475	335	140	
16	ADANI U 4	1	22	467	432	35	AS PER SYSTEM CONDITION
		91	96	467	432	35	
17	ADANI U 5	1	22	465	432	33	AS PER SYSTEM CONDITION
		88	96	465	432	33	
18	ADANI U 1	1	22	467	432	35	AS PER SYSTEM CONDITION
		90	96	467	432	35	
19	PARAS U-3 &4	1	22	460	332	128	AS PER SYSTEM CONDITION
		93	96	460	332	128	
20	JSW U-1	3	21	290	200	90	AS PER SYSTEM CONDITION
		96		290	273	17	
21	ADANI U-3	5	21	623	435	188	AS PER SYSTEM CONDITION

Note :

- 1 Above Statement is an abstract of Load Generation Balance as per Day Ahead Schedules, based on State Merit Order Despatch. Maximum backdown quantum during "Backing down Period" is indicated in the statement. Blockwise variations are available under "View Schedules".
- 2 MOD rates revised w.e.f 12-FEB-2015.
- 3 # Indicates that back down withdrawn due to Line loading/system constraints.