

**PROVISIONAL STATEMENT OF GENERATOR SCHEDULE (EX\_BUS) BACKED DOWN FOR THE DATE:15/10/2018**

SR. NO.	GENERATING STN. / STOA	Backing Down Period (in Time Block)		TARGET DESPATCHED SCHEDULE (in MW)		Backing Down Quantum [Max] in MW [A-B]	REMARK
		FROM	TO	Declared Capacity (A)	Despatched Schedule (Min) (B)		
1	SWPL TO MSEDCL(GMR)	X	X	X	X	X	Unit Running Below Tech Minimum
2	JSW U-2,3&4 TO MSEDCL DA	1	17	50	35	15	As Per System Condition
3	JSW U-2,3&4 TO MSEDCL DA	1	17	70	49	21	As Per System Condition
4	TPC U-8	1	17	204	187	17	As Per System Condition
5	DTPS REL U-1 & 2	1	17	456	336	120	As Per System Condition
6	TPC U-5	1	17	473	292	181	As Per System Condition
7	VIPL U-1 & 2	X	X	X	X	X	DC Less than Tech Minimum
8	NASIK U-3,4 & 5	1	17	284	283	1	As Per System Condition
9	PARALI U-6 & 7	X	X	X	X	X	NO BACK DOWN
10	BHUSAWAL U-3	X	X	X	X	X	NO BACK DOWN
11	PARALI U-8	X	X	X	X	X	NO BACK DOWN
12	RATTANINDIA U-1 TO 5 (PPA-1200 MW)	X	X	X	X	X	NO BACK DOWN
13	BHUSAWAL U-4 & 5	X	X	X	X	X	NO BACK DOWN
14	KHAPERKHEDA U- 1 TO 4	X	X	X	X	X	NO BACK DOWN
15	KORADI U-6 & 7	X	X	X	X	X	NO BACK DOWN
16	IDEAL ENERGY PROJECTS LTD. TO MSEDCL	X	X	X	X	X	NO BACK DOWN
17	DHARIWAL TO MSEDCL	X	X	X	X	X	NO BACK DOWN
18	KORADI U-8 ,9 &10	X	X	X	X	X	NO BACK DOWN
19	PARAS U-3 & 4	X	X	X	X	X	NO BACK DOWN
20	KHAPERKHEDA U-5	X	X	X	X	X	NO BACK DOWN
21	JSW-Ratnagiri U-1	X	X	X	X	X	NO BACK DOWN
22	ADANI (TIRODA 440 MW PPA) U 1,4 & 5	X	X	X	X	X	NO BACK DOWN
23	ADANI ,TIRODA U-1, 4 & 5 (PPA-1200 MW and 125 MW)	X	X	X	X	X	NO BACK DOWN
24	CHANDRAPUR U-3 TO 7	X	X	X	X	X	NO BACK DOWN
25	CHANDRAPUR U-8 & 9	X	X	X	X	X	NO BACK DOWN
26	ADANI (TIRODA 1320 MW) U 2 & 3	X	X	X	X	X	NO BACK DOWN

**Note :**

- 1 Above Statement is an abstract of Load Generation Balance as per Day Ahead Schedules, based on State Merit Order Despatch. Maximum backdown quantum during "Backing down Period" is indicated in the statement. Blockwise variations are available under "View Schedules".
- 2 M.O.D. RATES REVISED FROM DT. 12TH SEPT-2018
- 3 # Indicates that back down withdrawn due to Line loading/system constraints.